

Coffee break (15:10-15:50)

11. ``By silicon carbide avalanche diode 15:50-16:20
Surge suppression during switching” Masayuki Yamamoto (AIST/University of Yamanashi)
12. Special lecture 16:20-16:50
“Design of integrated WBG power electronics” Alberto Castellazzi (Kyoto University of Advanced Science)
14. Closing remarks 16:50-17:00

Jun Suda (Subcommittee Secretary-General)

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